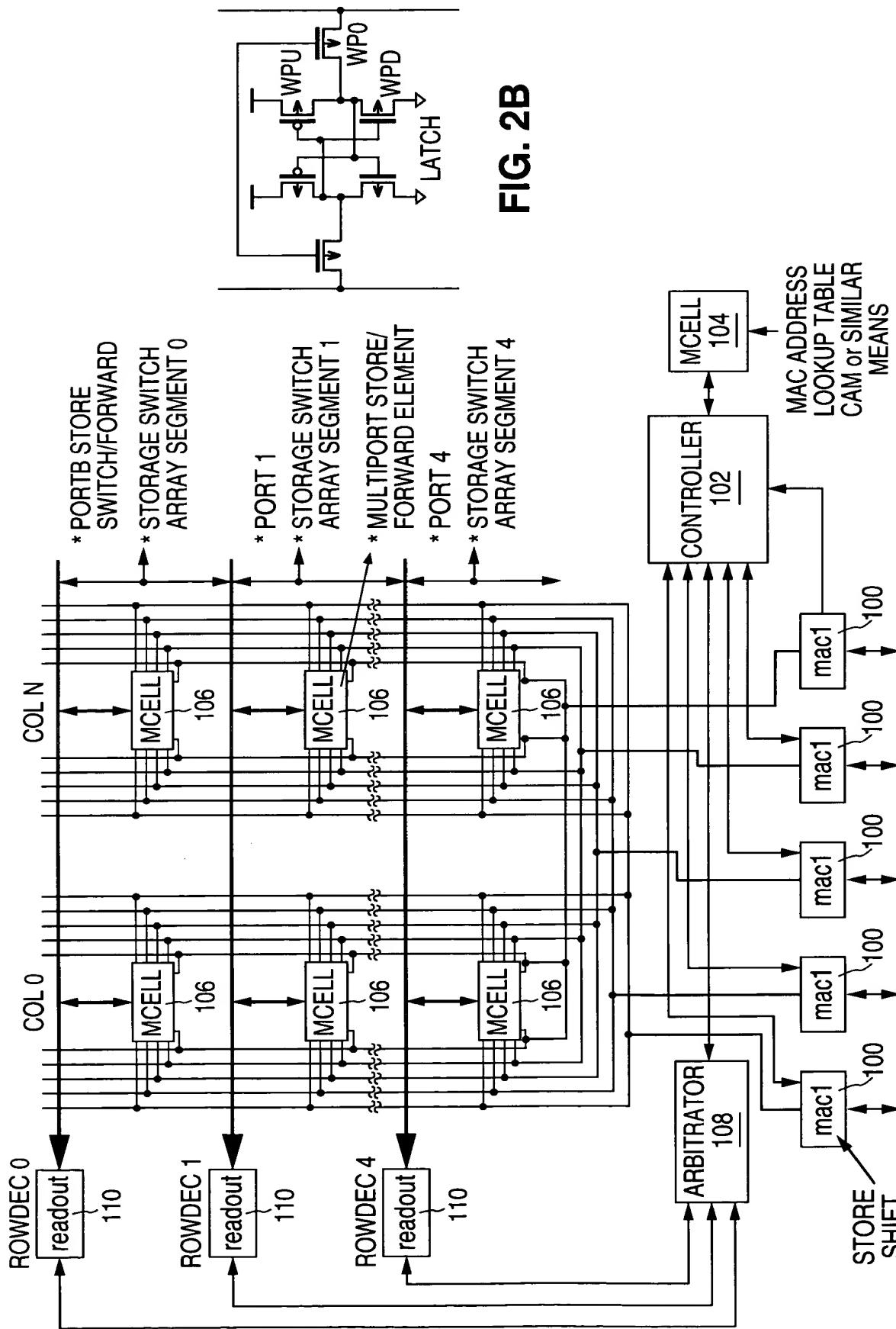


FIG. 1
 (PRIOR ART)

FIG. 2A



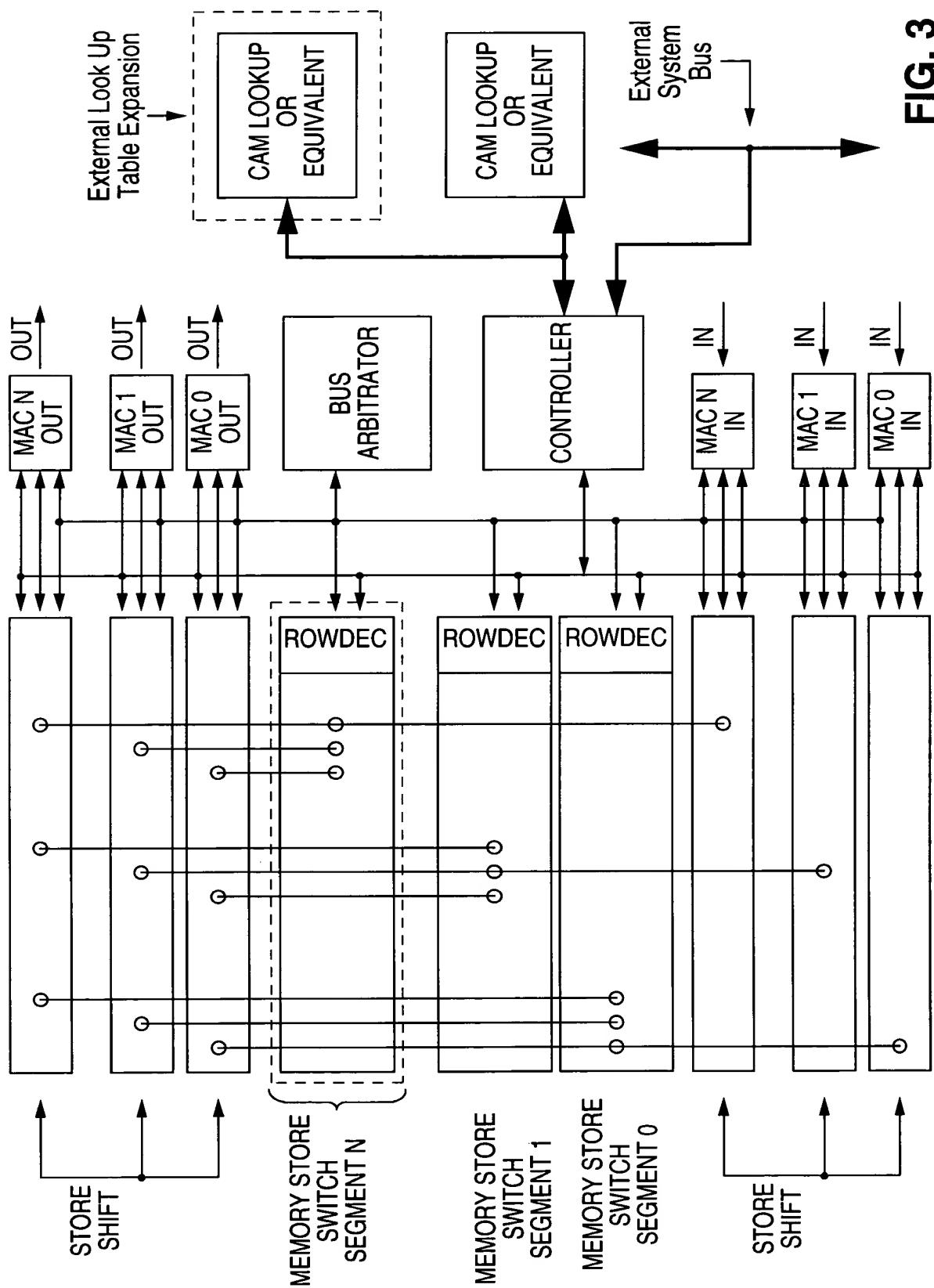
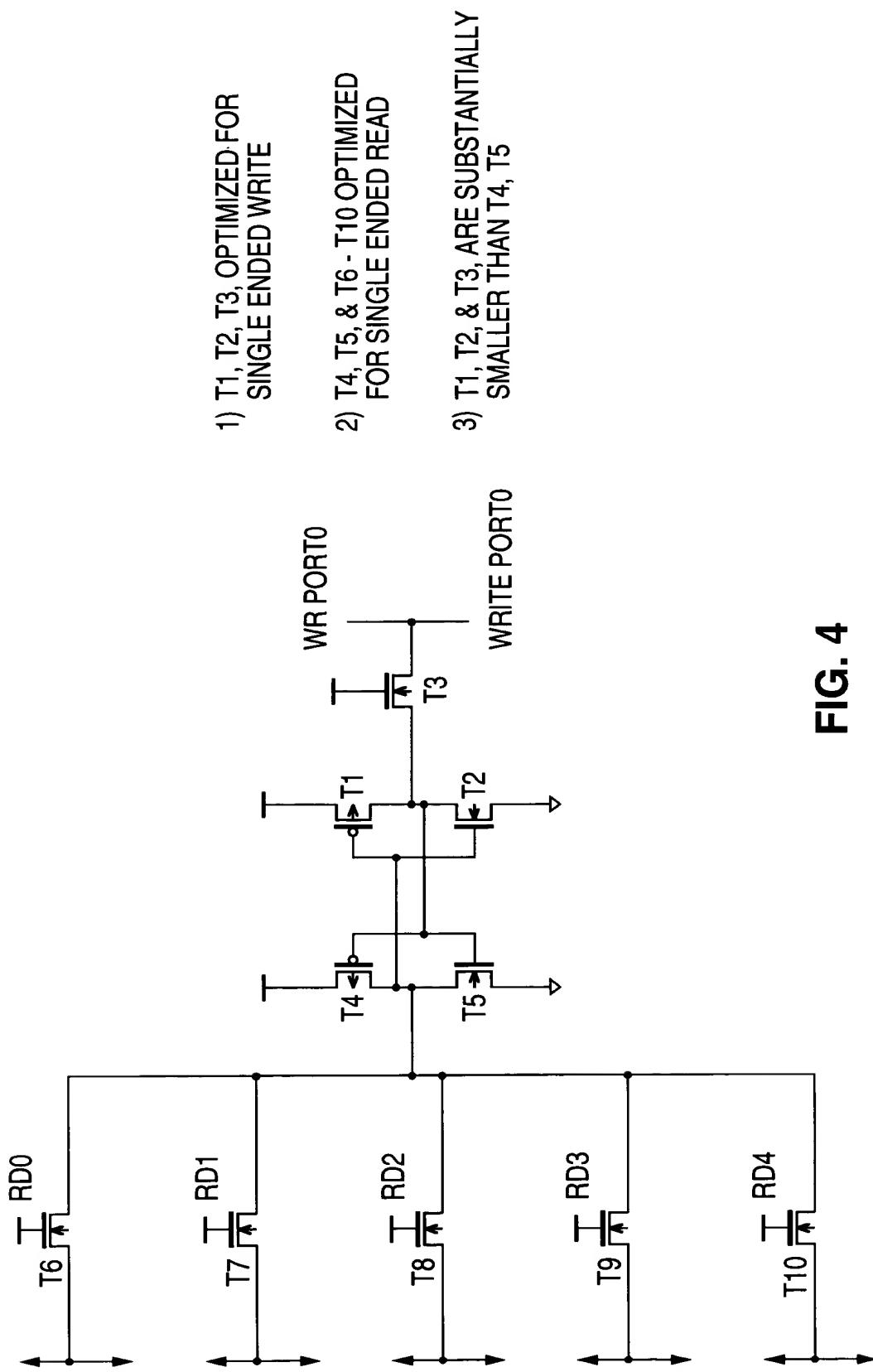


FIG. 3



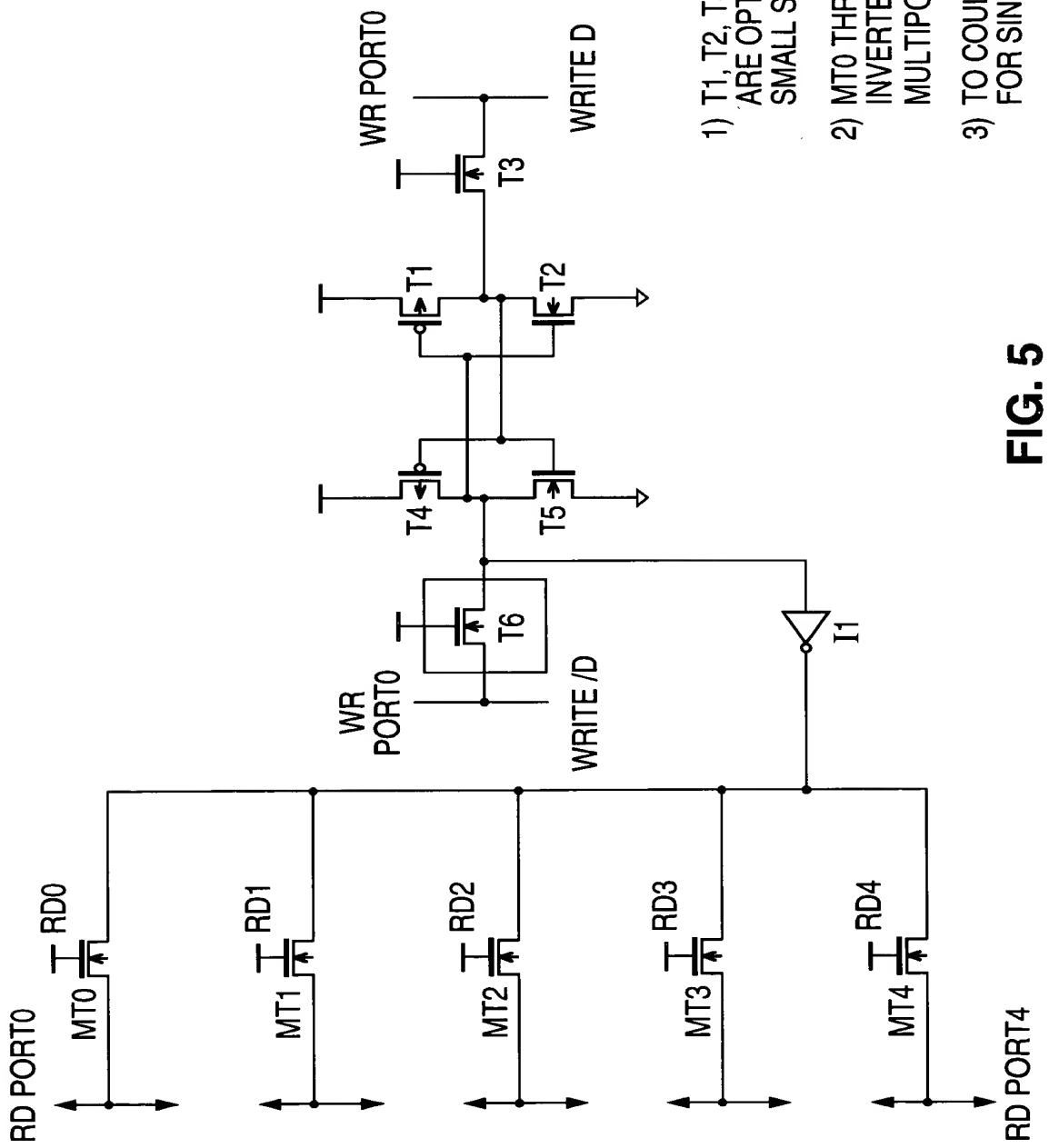


FIG. 5

RD PORT4

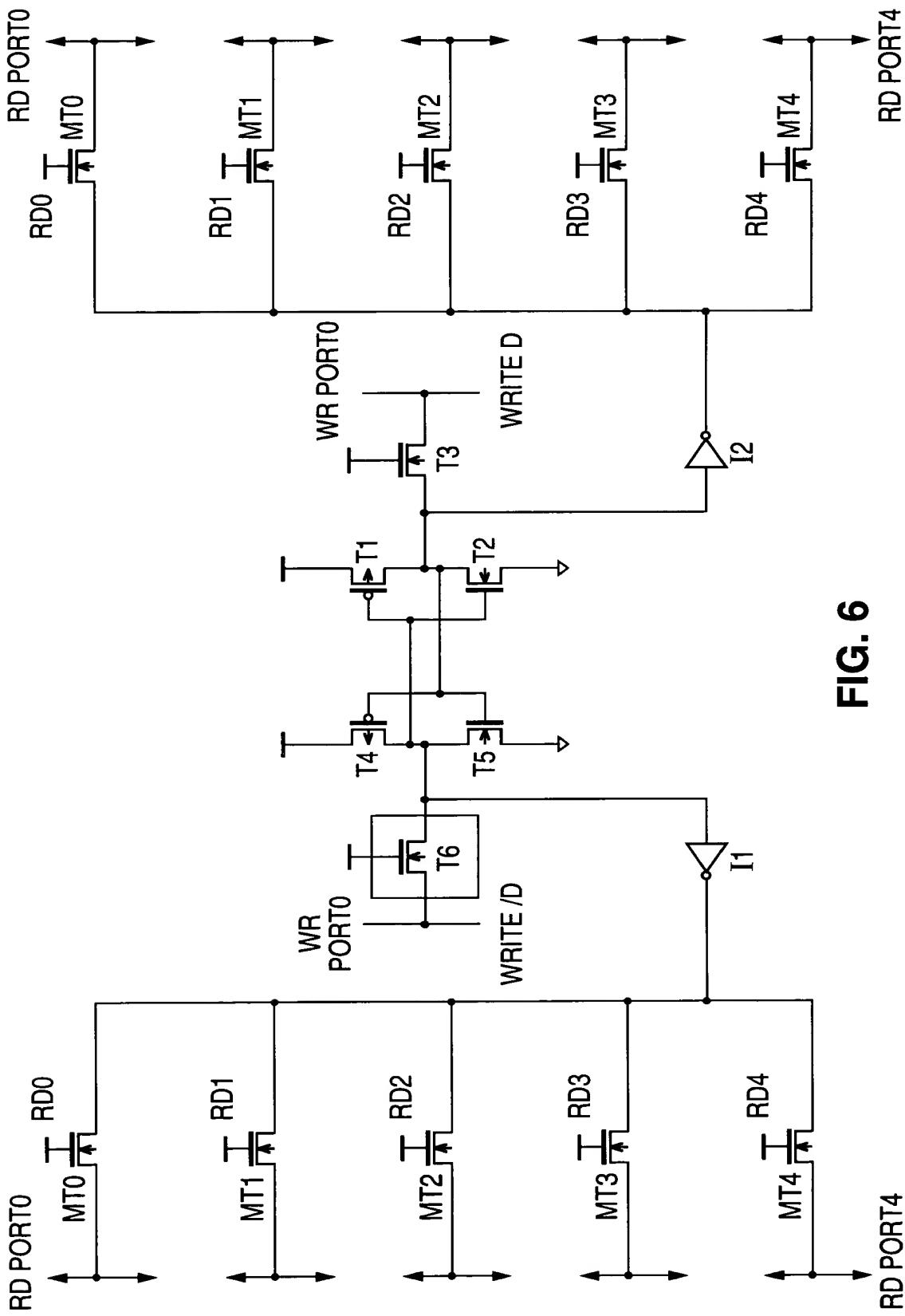


FIG. 6